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|------|-----------|------------|------------|------------|------------|
| LDN | A1 to LCD | TIANMA | Evr bouget | Ateam 8Bit | Ateam 4Bit |
| LDN0 | E | E1 | E1 | E1 | E1 |
| LDN1 | RV | E2 | E2 | E2 | N/C |
| LDN2 | RS | A0 | A0 | A0 | A0 |
| LDN3 | RESETB | RES | RES | N/C | N/C |
| LDN4 | CS1B | LCD_BRIGHT | LCD_BRIGHT | LCD_BRIGHT | LCD_BRIGHT |
| LDN5 | POWER | LCD_PON | LCD_PON | LCD_PON | LCD_PON |

| | | | | | |
|------|-----------|--------|------------|------------|------------|
| | A1 to LCD | TIANMA | Evr bouget | Ateam 8Bit | Ateam 4Bit |
| LCDD | | | | | 7:4 |
| | | | | | IR100? |

Title: Reciva Barracuda PCB - TOP
 Size: A4 Date: 18 nov 2006 Rev: a
 KiCad E.D.A. EESchema (2006-06-26) Sheet: 1/1